

Jointly Organized by



Electronics & ICT
Academy
MNIT Jaipur

<http://www.mnit.ac.in/eict>

LNMIIT
The LNM Institute of
Information Technology
The LNMIIT,
Jaipur

Advances in

VLSI & Embedded Systems

29th Mar. – 2nd Apr. 2018

Venue: The LNMIIT, Jaipur

<http://www.lnmiit.ac.in>

Faculty Development Programme
Sponsored by



Department of Electronics
& Information Technology
Ministry of Communications
& Information Technology
Government of India



Ministry of Electronics & Information Technology
Government of India

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Preamble (Electronics & ICT Academy)
Government of India had announced a National Policy on Skill Development, which has set a target of skilling 500 million people by 2022 in the domain of Electronics & IT. Under the plan scheme of “Digital India Manpower Development”. DeitY has set up seven (07) Electronics and ICT Academies as a unit in 03 IITs, 03 NITs and 01 IIIT with an objective of faculty/mentor development/up gradation in the areas related to Electronics & ICT leading ultimately to improved employability of graduates/diploma holders. MNIT Jaipur has set up such an academy for providing specialized training to faculty and industry persons in the states/UTs of Rajasthan, Gujarat, Daman & Diu, Dadra Nagar Haveli.

(A) Issues-

1. IT Hardware and Electronics Manufacturing industry- availability of properly trained, skilled and qualified manpower
2. Number of quality PhDs generated in IT / Computer Science is very low
3. In E & ICT domain- there is a very high degree of obsolescence of existing technologies and faster emergence of newer technologies

(B) Approach-

1. A focused faculty training/updation Programme for IT, Electronics and related sectors
2. Spreading up and continuous updation regarding Emerging Technology
3. Training and consultancy services for Industry
4. Design, Develop and Deliver specialized modules for specific research areas and Industry
5. Providing advice and support for technical incubation and entrepreneurial activities

An intensive one-week training programme is being organized for faculty of engineering and technological institutions. It is also open to persons from industry and doctoral students of Indian organizations. The objective is to provide an exposure to the participants to the state-of-the-art in **VLSI & Embedded Systems** through interaction with experts from CFTI institutions including host institutions. The technical program will include state-of-the-art lectures, hands-on lab sessions, tool demonstrations, and discussion/presentation sessions in the afternoons **from 9 AM to 5:30 PM**.

Course modules

- A. Digital System Design and implementation using Vivado
- B. Synthesize a RTL Design, IP integration, Hardware Debugging
- C. Partial Reconfiguration design using Zed/Zybo board
- D. Zynq architecture, Embedded System design using Zynq-7000, SDK
- E. Analog IC Design using Cadence.
- F. Embedded System interfacing and programing
- G. Real Time Operating Systems and Linux kernel

Invited Experts – Prof. Subir Kumar Roy- IIIT Bangalore, Mr. Anil Kr. Saini - Senior Scientist CEERI Pilani & others

Programme coordinators

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Registration

Registration is open to faculty, industry persons, doctoral and postgraduate students of programmes related to Electronics and Communication/ Computer Engineering. Participants will be admitted on a first-come first-served basis. Last date of registration is 18th Mar. 2018. The seats are limited to 30 participants, so participants will be admitted on first cum first served basis. Register on- line at http://www.mnit.ac.in/eict/apply_now.php

Fee

- (A) The one-time registration fee of Rs. 500/- is to be paid by each participant attending first time, irrespective of affiliation. This fee is not applicable for those participants, who have attended any Academy training programme earlier.
- (B). (i) The participants from academia and research scholars are required to pay a further fee of Rs. 2000/- (faculty/research-scholars). Rest expenditure is sponsored by DeitY through Electronics & ICT Academy at MNIT Jaipur.
- (B). (ii) However, the participants from industries, UG/PG students would pay a further fee of Rs. 5000/-.
- (B). (iii) Relaxation/rebate of 50% of course fee in (B)(i) and (ii) is applicable for SC/ST candidates.
- (C). The fee covers the participation in the programme, registration material including tutorial notes, tea and working lunches on all the days of the workshop. The travel and other expenses would have to be borne by the participants or their parent-organizations.
- (D). Lodging for very limited number of outside participants is also available in Hostels, LNMIIT Jaipur on payment of Rs 100 per day.
- (E) The organizers should receive the registration amount through online payment/NEFT/IMPS/DD.
- (F). Please pre-intimate your desire to participate through e-mail, before registration form reaches us.

Account Name- 'Electronics and ICT Academy MNIT Jaipur'	Account Number- 676801700483
Bank address- ICICI Bank, MNIT Campus Branch, Jaipur	IFSC Code- ICIC0006768

Further query:

- (a) Please visit us at: <http://www.mnit.ac.in/eict>,
<http://academymnit.wordpress.com>
- (b) Email us: academy@Tjmnit.ac.in